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PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/628,867	07/27/2000	Terry M. Laviolette	0100.0001320	2885
7590 11/12/2003		EXAMINER		
Markison & Reckamp PC			HO, THE T	
PO Box 06229 Wacker Drive			ART UNIT	PAPER NUMBER
Chicago, IL 60606-0229			2126	

DATE MAILED: 11/12/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
065 4-45 0	09/628,867	LAVIOLETTE ET AL.					
Office Action Summary	Examiner	Art Unit					
	The Thanh Ho	2126					
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet w	ith the correspondence address					
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a ly within the statutory minimum of thi will apply and will expire SIX (6) MOI e, cause the application to become A	reply be timely filed ty (30) days will be considered timely. NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).					
Status 1) Page and it to a communication (a) filled on 27	lulu 2000						
1) Responsive to communication(s) filed on 27.							
/ -	nis action is non-final.	44					
 Since this application is in condition for allow closed in accordance with the practice under Disposition of Claims 							
4) Claim(s) 1-20 is/are pending in the application	n.						
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1-20</u> is/are rejected.							
7) Claim(s) is/are objected to.	7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/o	or election requirement.						
Application Papers							
9)☐ The specification is objected to by the Examine	er.						
10)☐ The drawing(s) filed on is/are: a)☐ acce	pted or b) objected to by	the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11)☐ The proposed drawing correction filed on is: a)☐ approved b)☐ disapproved by the Examiner.							
If approved, corrected drawings are required in reply to this Office action.							
12) The oath or declaration is objected to by the Ex	kaminer.						
Priority under 35 U.S.C. §§ 119 and 120							
13) Acknowledgment is made of a claim for foreig	n priority under 35 U.S.C.	§ 119(a)-(d) or (f).					
a) ☐ All b) ☐ Some * c) ☐ None of:							
 Certified copies of the priority documen 	ts have been received.						
2. Certified copies of the priority documen	ts have been received in A	Application No					
 3. Copies of the certified copies of the price application from the International But See the attached detailed Office action for a list 	ureau (PCT Rule 17.2(a)).	-					
14) ☐ Acknowledgment is made of a claim for domest	•						
a) The translation of the foreign language pr	• •						
Attachment(s)	· •						
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 	5) Notice of	Summary (PTO-413) Paper No(s) Informal Patent Application (PTO-152)					

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DETAILED ACTION

1. This action is in response to the application filed 7/27/2000.

2. Claims 1-20 have been examined and are pending in the application.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Admitted Prior Arts (APA) in view of Park U.S Patent No. 6,081,891.

As to claim 1, APA teaches first and second video adapter (primary and secondary adapters, lines 26-27 page 3) with first and second video BIOSs (video BIOSs, line 14 page 3); a read only memory storing a system BIOS (BIOS in a ROM, lines 14-15 page 2) causes the system to identify one of the video adapters as a primary and the other as a secondary (primary and secondary adapters, lines 24-28 page 3); the system executing a POST during the system BIOS (executes a self test program referred to as POST, line 14 page 2); video adapters are used to display information on display devices (multiple video adapters for running multiple display devices, line 10 page 3). APA does not explicitly teach a processing unit and a memory. However, APA teaches the system is implemented on a typical computer (line

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13 page 2). "Official Notice" is taken that both the concept and advantage of providing for a processing unit and a memory is well known and expected in the art. It would have been obvious to include these units into the system of APA because these units provide proper working of a typical computer system. APA does not explicitly teach the step of storing the BIOSs.

Park teaches a system of multiple video BIOSs (video BIOS and video card BIOS, lines 38-46 column 3) wherein the system stores the first video BIOS (video BIOS, line 39 column 3) in a first memory area (RAM 210, line 38 column 4), copies the first video BIOS to a second memory area (ROM 220, line 36 column 4) when the second video adapter (video card BIOS, line 48 column 4) is present in which the second video BIOS (video card BIOS, line 48 column 4) being stored in the first memory area (loads to RAM 210, lines 48-49 column 4); a video driver (video card controller, line 35 column 4) copying the first video BIOS to a third memory area (lines 33-45 column 4). It would have been obvious to apply the teachings of Park to the system of APA because this provides the system the ability to use multiple video BIOSs in the system when a new video BIOS is present, therefore prevents wasting of valuable memory space as disclosed by Park (lines 15-20 column 2).

As to claim 2, APA as modified further teaches a motherboard stored the first video BIOS and the first video adapter (BIOS on the motherboard, lines 24-25 page 3).

As to claim 3, APA as modified further teaches an add-on board stored the second video BIOS (add-on video card, line 25 page 3); the first video BIOS is stored on the motherboard in compressed form (BIOS stored in compressed form, lines 24-25

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page 3). Park further teaches the first video BIOS is stored in the first memory area in decompressed form (loads video BIOS to RAM 210, lines 37-38 column 4). Note the discussion of claim 1 for reasons of combining references.

As to claim 4, note the discussion of claim 3 above.

As to claim 5, Park further teaches the second video BIOS is stored in uncompressed form (lines 33-45 column 4). Note the discussion of claim 1 for reasons of combining references.

As to claim 6, APA as modified further teaches a portable computer (portable computer, line 24 page 3) having the first video adapter (video adapter on the motherboard, lines 26-27 page 3). APA does not explicitly teach a docking station. However, APA (lines 24-28 page 3) discloses that the portable computer connected to a system in which the add-on video card of this system is used as primary. It would have been obvious to conclude that the system is having a docking station; therefore the portable computer can be connected the system through the station to use the add-on video card.

As to claim 7, APA as modified further teaches a first display device and a second display device (multiple display devices, line 10 page 3).

As to claim 8, note the discussions of claims 6-7 above.

As to claim 9, Park further teaches the second memory is a frame buffer (210, Fig. 3). It would have been obvious to apply the teachings of Park to the system of APA because this provides the memory space for the video card BIOS to execute in the system as disclosed by Park (lines 34-42 column 2).

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As to claim 10, note the discussions of claims 1-3 and 5 above.

As to claims 11-13, note the discussions of claims 6-8 above, respectively.

As to claim 14, note the discussion of claim 1 above.

As to claim 15, note the discussions of claims 2-3 above.

As to claim 16, note the discussion of claim 10 above.

As to claim 17, note the discussions of claims 2-3 and 5 above.

As to claim 18, note the discussion of claim 6 above.

As to claim 19, note the discussions of claims 7-8 above.

As to claim 20, note the discussion of claim 1 above.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to The Thanh Ho whose telephone number is 703-306-5540. A voice mail service is also available for this number. The examiner can normally be reached on Monday – Friday, 8:30 am – 5:00 pm.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

Any response to this action should be mailed to:

Commissioner for Patents

P.O Box 1450

Alexandria, VA 22313-1450

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Or fax to:

- AFTER-FINAL faxes must be signed and sent to (703) 746 7238
- OFFICAL faxes must be signed and sent to (703) 746 7239
- NON OFFICAL faxes should not be signed, please send to (703) 746 7240

TTH November 7, 2003

> JOHN FOLLANSBEE SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100